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| **Computer Architecture – List of Programs** | |
| **Exp No** | **TITLE** |
| **1.** | 8-BIT ADDITION |
| **2.** | 8-BIT SUBTRACTION |
| **3.** | 8-BIT MULTIPLICATION |
| **4.** | 8-BIT DIVISION |
| **5.** | 16 -BIT ADDITION |
| **6.** | 16-BIT SUBTRACTION |
| **7.** | 16-BIT MULTIPLICATION |
| **8.** | 16-BIT DIVISION |
| **9.** | FACTORIAL OF A GIVEN NUMBER |
| **10.** | LARGEST NUMBER IN AN ARRAY |
| **11.** | LOGICAL GATES |
| **12.** | TWO BIT HALF ADDER |
| **13.** | TWO BIT HALF SUBTRACTOR |
| **14.** | FULL ADDER |
| **15.** | FULL SUBTRACTOR |
| **16.** | SMALLEST NUMBER IN AN ARRAY |
| **17.** | ASCENDING ORDER |
| **18.** | DESCENDING ORDER |
| **19** | ADDITION OF N NUMBERS |
| **20** | SWAPPING OF NUMBERS |
| **21** | SQUARE OF NUMBER |
| **22** | ONEs AND TWOs COMPLEMENT |
| **23** | ROTATE LEFT OPERATION |
| **24** | ROTATE RIGHT OPERATION |
| **25** | LOGICAL OPERATIONS |
| **26** | DECIMAL TO BINARY CONVERSION |
| **27** | DECIMAL TO HEXADECIMAL CONVERSION |
| **28** | DECIMAL TO OCTAL CONVERSION |
| **29** | BINARY TO DECIMAL CONVERSION |
| **30** | HEXA DECIMAL TO DECIMAL CONVERSION |
| **31** | BINARY TO OCTAL CONVERSION |
| **32** | OCTAL TO BINARY CONVERSION |
| **33** | BINARY TO HEXADECIMAL CONVERSION |
| **34** | HEXADECIMAL TO OCTAL CONVERSION |
| **35** | HEXADECIMAL TO BINARY |
| **36** | CPU PERFORMANCE |
| **37** | SINGLE PRECISION |
| **38** | TWO STAGE PIPELINE (add,sub,mul,div) |
| **39** | BOOTHS ALGORITHM |
| **40** | INTEGER RESTORATION DIVISION |